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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Mansoori et al.

Art Unit: 2812

Serial No.: 10/620,492

Examiner: Lindsay, Jr., W.

Filing Date: 07/16/2003

Docket No.: TI-35375

Customer No.: 23494

Conf. No.: 9069

Title: METHOD TO REDUCE TRANSISTOR GATE TO SOURCE/DRAIN OVERLAP
CAPACITANCE BY INCORPORATION OF CARBON

CERTIFICATION OF FACSIMILE TRANSMISSION

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AMENDMENT (10 Pages)
 EOT 3mth (2 Pages)
 NOTICE OF APPEAL (# Pages)
 APPEAL (# Pages)
 ISSUE FEE (# Pages)
 REPLY BRIEF (IN TRIPPLICATE) (# Pages)

NAME OF INVENTOR(S):

Mansoori

TITLE OF INVENTION:

Method to Reduce Transistor . . .

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TI-35375

DEPOSIT ACCT. NO.:

20-0668

FAXED: 5/31/2005

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